

# Alexander J Rutledge

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## OBJECTIVE

Enthusiastic new graduate desires to achieve a career position at a company deeply involved in the automation and/or optimization of computer hardware. Accompanied by strong work ethic and genuine passion for the industry.

## EDUCATION

Northern Arizona University, expected graduation in Spring 2015

### **Bachelor of Science, Electrical Engineering**

*Computer Engineering Emphasis*

- Comm. Systems
- Signals & Systems
- Microprocessors
- Power Electronics
- Electromagnetics
- VHDL Design
- Embedded Control
- Digital Logic
- Data Structures
- All Relevant Math Courses

## PROJECTS

**Wi-Fi Mapping / Communication**, Cobham Aerospace, *Oct.2014 - Cur.*

- Senior Design Project
- Mobile phone application allowing voice and GPS communication between customizable subgroups.

## TECHNICAL SKILLS

- **Programming Languages:**
  - Java, Python, C, VHDL, Assembly
- **Software:**
  - Altera Quartus II
  - Altera Modelsim
  - PSpice
  - Mentor Graphics
  - ANSYS Maxwell
  - Matlab
  - NetBeans
  - Code Composer Studio
- Extensive educational instruction regarding the simulation of circuit design and communication systems.
- Proficient with the wide variety of uses and applications of computers and computer systems.
- Skilled in the diagnosis and troubleshooting of issues associated with I.T.
- Excellent background in critical thinking/problem solving.
- Self-motivated and willing to take on challenging projects.
- Highly results oriented.