

# Josiah Hutchinson

Address: 1515 S. Yale St. Flagstaff, AZ 86001

E-Mail: [josiahdhutchinson@gmail.com](mailto:josiahdhutchinson@gmail.com)

Phone: (520) 266-3326

---

## Objective

Pursuing a PhD at the University of Arizona

## Education

**Northern Arizona University, Flagstaff AZ – Undergraduate**

**Fall 2010 to Present**

- B.S. in Electrical Engineering (with Computer Emphasis)
- Expected graduation date: May 2014
- Maintaining a 4.0 GPA
- Received a full tuition scholarship for any of the three major Arizonan schools

## Research Experience

**Columbia University, New York City NY – Undergraduate Researcher**

**Summer 2013**

- Researched topics in wireless networks as a part of the Center for Integrated Access Networks REU
- Tested a wireless node selection algorithm on a large scale wireless test bed called ORBIT
- Parallelized a python script across a cluster that data mined packet capture files from the ORBIT tests
- Used Git as version control for iterations of the python script and ORBIT tests
- Presented my results to the faculty, PhD students, and other undergraduate researchers

## Work History

**Northern Arizona University EE & CS Department, Flagstaff AZ – Lab TA & Grader**

**Fall 2011 to Present**

- Guided students through digital logic, circuits and semiconductor labs by explaining necessary concepts and helping with debugging
- Developed an FPGA lab on the Quartus II software with another TA to replace an outdated lab
- Debugged a piece of software written in Java for a lab on timing diagrams
- Worked professionally and sociably with various professors and colleagues throughout

**W. L. Gore & Associates, Flagstaff AZ – Design & Automation Intern**

**Summer 2012**

- Developed a recipe transfer system for a variety of machines using Excel, VBA, RSLogix5000 and FactoryTalk ME
- Worked with a sponsor to design general process code for a Next Generation Laser machine
- Researched and tested the abilities of Yokogawa's data acquisition devices
- Attended a training course on AutoCADe and helped another intern develop schematics
- Gave a PowerPoint slide presentation on these accomplishments to the Design & Automation Engineers

## School Experience

- Taken or taking classes that address microprocessors, electronic circuits, data structures, algorithms, engineering design (3 classes), digital logic, electromagnetics, engineering analysis, semiconductor memories, digital signal processing, computer architecture, and many other technical topics
- Proficient in Design Architect, Quartus II, Eclipse, Assembly, Matlab, VHDL, Python, and Java
- Currently a member of IEEE at Northern Arizona University
- Previously the treasurer of the FIRST Robotics club at Buena High School in Sierra Vista, AZ
  - Programmed and designed robots/created animations using 3ds Max as a part of the FIRST team 1726
  - Participated in the FIRST nationals and won several team awards including the Motorola Quality Award, the Autodesk Visualization Award, and the Delphi Driving Tomorrow's Technology Award