# Daniel Roberts

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## EDUCATION

**Northern Arizona University, Flagstaff, AZ**                          August 2010 – May 2014
Bachelor of Science, Electrical Engineering

4.00 Cumulative GPA

## WORK EXPERIENCE

**Northern Arizona University, Flagstaff, AZ**                          August 2013 – December 2013
*Electronics Lab Assistant* – Attended weekly labs and taught students circuit design, IC layout, and frequency/transient/DC simulation using Mentor Graphics EDA tools. Responsible for grading lab assignments outside of allocated lab time.

**Intel Corporation, Dupont, WA**                          May 2013 – August 2013
*Intern, Intel Enterprise Power Solutions* – Responsible for design of experiments focused on lossless measurement of processor transient load. Key item of interest was to detect, and measure peak repetitive transients and determine the frequency spectrum of transient load current. Board parasitics were calibrated using purpose built test tools and measurements in time and frequency domains. Used MATLAB signal processing tools for data processing and analysis. Presented results to engineering staff.

**Northern Arizona University, Flagstaff, AZ**                          August 2012 – December 2012
*Electrical Engineering Lecture Aide* – Attended class and provided tutoring hours available to all students in the course. Material taught included fundamental electrical circuit concepts of voltage, charge, current, power and energy, and their inter-relationships along with DC and AC linear circuit analysis.

**SPAWAR Systems Center Pacific, San Diego, CA**                          June 2012 – August 2012
*NREIP Research Intern* – Worked with the navigation branch dealing with signal processing and the implementation of various sensors into navigation systems for the Navy. Research involved programming and running simulations in MATLAB along with analysis of different approaches to implementing Kalman filters. Presented research to an audience of engineers and researchers at the facility.

## SKILLS

MS Office (Word, Excel, and PowerPoint)

Mentor Graphics (similar to Cadence) – VLSI, IC Layout

MATLAB, C/ C++, Assembly Language, Tcl

Digital Signal Processing

VHDL (Quartus and Modelsim-Altera), FPGA

## HONORS, AWARDS, AND MEMBERSHIPS

Institute of Electrical and Electronics Engineers

Cloud Computing and Big Data Analysis Class Completion (SPAWAR Systems Center Pacific)

Daie Endowment Scholarship, Follett's Bookstore Scholarship, Llewellyn R. Pierce Scholarship

Dean's List 2010-2014